

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kobayashi, Kobayashi

Serial No.: 10/060,569

Filed: January 29, 2002

Title: METHOD OF FABRICATING A  
SEMICONDUCTOR DEVICE

Attorney Docket No.: N230500C1

Group Art Unit: 1763

Examiner: Goudreau, George A.

#7  
10/23/02  
NW

## REQUEST FOR INTERVIEW

5 Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

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Applicants respectfully request a phone interview to discuss various matters related to the  
present application.

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37 C.F.R. §1.8

I hereby certify that this correspondence is being

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[X] transmitted via facsimile to the United States Patent and Trademark Office to fax number: 1-703-872-9310Date of Transmittal: October 21, 2002

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[ ] deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for  
Patents, Washington, D.C. 20231.

Date of Deposit: \_\_\_\_\_

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Typed/Printed Name: Bradley T. SakoSignature: *Bradley T. Sako*FAX RECEIVED  
OCT 22 2002  
GROUP 1700  
OFFICIAL

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**Issues for Interview

We would like to discuss the following issues:

- 1) The patentability newly submitted claims 41-48 (filed 10/15/02 via fax).
- 2) The basis for modifying the reference *Barth et al.* ("Advanced PVD Ti/TiN Liners for Contact and Via Applications, SPIE, Vol. 3214 (1997)) in order to show the limitation

"etching back a conductive layer to form a plug that has a top surface which projects above a top surface of an insulating layer"

- 3) The patentability of claim 35, in light of the reference teachings.

Contact for Interview

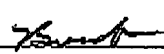
We would greatly appreciate it if the following attorney of record could be contacted to arrange for the interview.

Brad Sako

Phone: (408) 289-5315

Fax: (408) 977-0174

Respectfully Submitted,

 10/21/02  
Bradley T. Sako  
Reg. No. 37,923

Bradley T. Sako  
WALKER & SAKO, LLP  
300 South First Street  
Suite 235  
San Jose, CA 95113  
Tel. 1-408-289-5315